



**UNIVERSITY OF PUNE**  
**CENTRE FOR MODELING AND SIMULATION**  
PUNE 411 007 INDIA

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**AMENDMENTS**

in

a Tender Document Reference # CMS/1415/053 Dated  
27/05/2014

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- (1) Page 5/67, Section Number 1, point 8 should be read like ...

The bids must reach to the above mentioned address on or before August 1, 2014 at 15:00 Hrs (IST).

- (2) Page 5/67, Section Number 1, Add following point as 8(a) ...

8(a). Technical bids will be opened on August 1, 2014 at 15:30 Hrs. (IST)

- (3) Page 8/67, point 7(b) should be read like ...

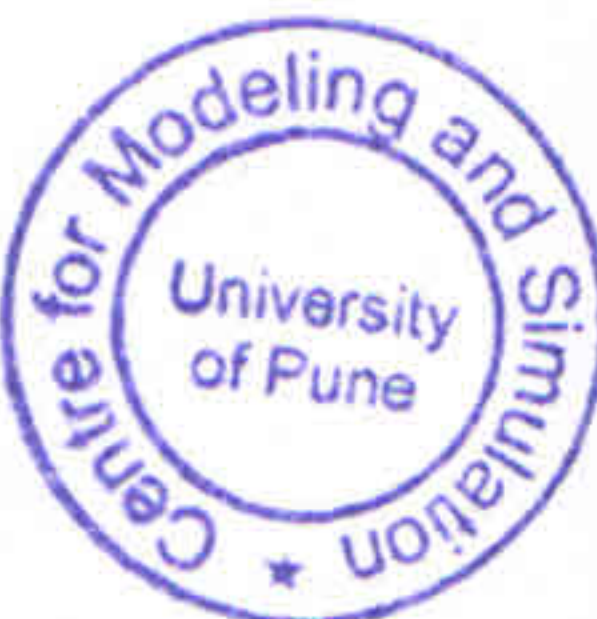
In one bid, bidder can propose only one type of architecture with the specific processor mentioned in the bid. If bidder wishes to propose different type of architecture then it needs to be proposed as a separate bid.

- (4) Page 8/67, point 7, add following point as 7(h) ...

The bid with requested processor will be treated as the primary bid. In addition to this, bidder needs to submit a separate supporting bid with next generation processor and has to ensure that respective processor manufacturers give us application benchmark results. Failure to do so may result in rejection of your supporting bid. This may also result in disqualification if the technical committee decides to opt for the next generation processor.

- (5) Page 8/67, point 8(a) should be read like ...

The Bidder/OEM shall furnish, as part of its bid, a EMD/bid security for an amount ₹ 15 Lakhs in the format of Bank guarantee (BG) or Demand Draft (DD). The BG should be valid for 60 days beyond the validity of the bid. The DD should be drawn on any Nationalized bank in the favour of "The Registrar, University of Pune, Pune", payable at Pune.





- (6) Page 15/67, point 6(a) should be read like ...

The successful Bidder/OEM will be required to furnish, a security deposit for an amount of 5% of the total order value within 15 days of the date of contract/order, in the format of Bank guarantee.

- (7) Page 16/67, point 7(a) should be read like ...

Before claiming the final settlement, the successful bidder/OEM will be required to furnish the Performance Bank Guarantee in the form of BG for 10% amount of the Order, as per the format (on page 63) attached to this document.

- (8) Page 26/67, Section number 4, Scope of work, Part-II: Data Center facility; Add following point as (16) ...

(16) Foundation, shade and Metal cage for diesel generator and chillers.

- (9) Page 26/67, Section number 4.1, point number 1; System Architecture should be read like ...

- Tightly integrated cluster with 64 bit processor Intel Xeon E5-2695 V2 series 12 core at 2.4 GHz clock speed / Intel Xeon E5-2697 V2 series 12 core at 2.7 GHz clock speed as a primary bid, and next generation processor (minimum 12 core and 2.4 GHz from Intel) as a supporting bid, connected with FDR/QDR80 Infiniband or equivalent proprietary interconnect. CMS reserves the right to choose the processor.

OR

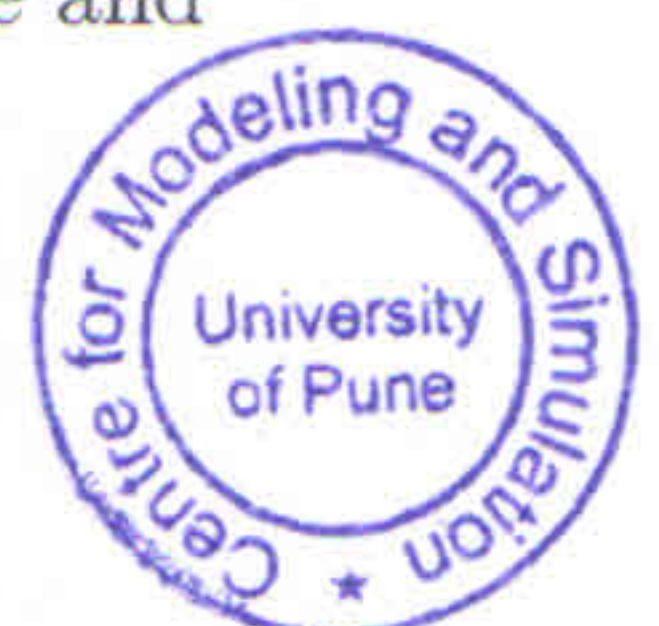
Tightly integrated cluster with 64 bit processor Power 7 at 4.1 GHz and 8 cores as a primary bid, and next generation processor (minimum 12 cores and 3.02 GHz from IBM) as a supporting bid, connected with QDR Infiniband or equivalent proprietary interconnect. CMS reserves the right to choose the processor.

- Only 1U or 2U size server will be accepted. Dense server / Chassis based solutions will be accepted. Blade servers will not be accepted.

- All the servers should have fully populated 2 CPU sockets per node.

- (10) Page 33/67, Section number 4.1, point number 28, Add following point ...

Home and Archive file system must be offered on NFS protocol such that these file systems are available inside the cluster on Infiniband and outside the cluster on 1Gbps Ethernet. I/O servers for home and archive must have both types of interface to facilitate such access. This is a mandatory requirement to enable access to Home and archive data on Linux workstations outside the cluster.





- (11) Page 34/67, Section number 4.1, point number 31, Add following point ...

Performance for each of the Scratch, Home and Archive file system must be demonstrated separately by running IOR/IOZone such that benchmarking tool does write operation (only) first, followed by read operations. Read and write are not to be run simultaneously. Bidders must meet stated write performance (when running write operation only) and read performance must be equal or more than write when run independently.

- (12) Page 44/67, Section number 7, Add following point as 1 (a) ...

(a) The application benchmarks submitted with primary bid will be evaluated and will be compared to find technically qualified bidders.

- (13) Page 45/67, Section number 7, Add following point as 5 (a) ...

Technical Committee will go through the technical part of supporting bids submitted by technically qualified bidders and will select the processor (requested / next generation) on technical grounds. CMS reserves the right to choose the processor.

- (14) Page 45/67, point 6(a) should be read like ...

Commercial bids of the selected processors shall be opened for the technically qualified bidders after the technical evaluation. The University will communicate the date and time of opening of the commercial bids only to the qualified bidders.



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Date: July 24, 2014

